

Microelectronic Circuit Design International Edition

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IIP- B.Tech (EE Dept.)

4 EE201 Digital Circuits and Microprocessor (3-0-0-6)
Pre-requisites: Nil Introduction to digital circuits:
Logic families (TTL and MOS), Number systems, Integer and floating point

Course structure of B. Tech. AI & DS, IIT Patna, CSE

Detailed Syllabus Semester I CE111 Engineering Drawing
1-0-3-5 Civil Geometrical construction of simple plane figure: Bisecting the line, draw perpendicular, parallel line, bisect angle, trisect angle, construct equatorial triangle, square, polygon, inscribed circle.